

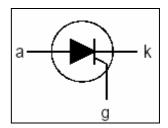
BT151 Series

THYRISTORS

FEATURE

Glass passivated thyristors in a plastic TO220 package. They are intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance.

Typical applications include motor control, industrial and domestic lighting, heating and static switching. Compliance to RoHS.



ABSOLUTE MAXIMUM RATINGS

Symbol	Ratings	Value		Unit		
		BT151-500R	BT151-650R	BT151-800R		
V _{DRM}	Repetitive peak off-state voltage	500	650	800	V	
V _{RRM}	Repetitive peak reverse voltage	500	650	800	V	
I _{T(RMS)}	RMS on-state current	12		Α		
I _{T(AV)}	Average on-state current	7.5		А		
I _{TSM}	Non-repetitive peak on- state current	100		Α		
P _{GM}	Peak gate power	5			W	
PG _(AV)	Average gate power	0.5		W		
T _{stg}	Storage temperature range	-45 to +150		°C		
T _j	Operating junction temperature	110		°C		

THERMAL CHARACTERISTICS

Symbol	Ratings	Value	Unit	
R _{∂j-mb}	Thermal resistance junction to mounting base	≤ 1.3	°C/W	
R _{∂JA}	Thermal resistance junction to ambient	≤ 60	C/VV	



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ELECTRICAL CHARACTERISTICS

TC=25°C unless otherwise noted

Symbol	Ratings	Test Condition(s)	Min	Тур	Max	Unit
	Repetitive peak	BT151-500R	500	-	-	
V_{DRM}	off-state voltage	BT151-650R	650	-	-	j
	on-state voltage	BT151-800R	800	-	-	V
	Repetitive peak reverse voltage	BT151-500R	500	-	-	V
V_{RRM}		BT151-650R	650	-	-	
		BT151-800R	800	-	-	
I _{GT}	Gate trigger current	$V_D = 12 \text{ V}; I_T = 100 \text{ mA}$	-	-	15	mA
V_{GT}	Gate trigger voltage	$V_D = 12 \text{ V}; I_T = 100 \text{ mA}$	-	-	1.5	V
IL	Latching current	$V_D = 12 \text{ V}; I_{GT} = 100 \text{ mA}$	-	-	40	mA
I _H	Holding current	V _D = 12 V; I _{GT} = 100 mA	-	-	20	mA
I _D	Off-state current	$V_D = V_{DRM \text{ max}}, T_j = 125^{\circ}C$	-	-	0.5	mA
I _R	Reverse current	$V_R = V_{RRM \text{ max}}, T_j = 125^{\circ}C$	-	-	0.5	mA
V _T	On-state voltage	I _T = 23 A	-	-	1.75	V

DYNAMIC CHARACTERISTICS

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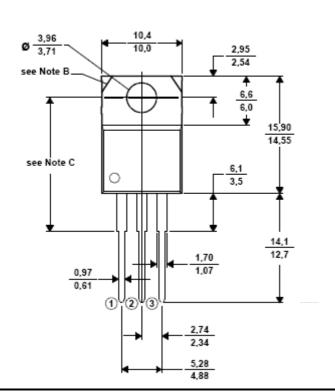
Symbol	Ratings	Test Condition(s)	Min	Тур	Max	Unit
-17/ /-14	Critical rate of rise of	V _{DM} = 67% V _{DRMmax} ; T _j = 125°C Exponential waveform; gate open circuit	50	130	-	V/µs
av-/at	off-state voltage	V_{DM} = 67% V_{DRMmax} ; T_j = 125°C Exponential waveform R_{GK} = 100 Ω	200	1000	-	V/µs
t gt	Gate controlled turn-on time	$I_{TM} = 40 \text{ A}; V_D = V_{DRMmax}$ $I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	2	-	μs
tq	Circuit commutated Turn-off time	V_{DM} = 67% V_{DRMmax} ; T_j = 125°C I_{TM} = 20 A; V_R = 25 V R_{GK} = 100 Ω dI_{TM}/dt = 30 A/ μ s dV_D/dt = 50 V/ μ S	-	70	-	μs

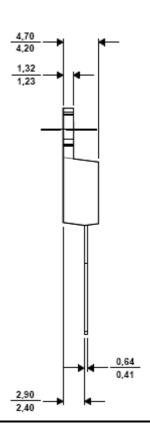


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MECHANICAL DATA CASE TO-220







Pin 1 :	Main Terminal 1
Pin 2 :	Main Terminal 2
Pin 3 :	Gate
Case :	Main Terminal 2

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